

Department of Electronics & Communication Engineering
DELHI TECHNOLOGICAL UNIVERSITY

(Formerly Delhi College of Engineering)
Shahbad Daultapur, Bawana Road-Delhi-42

DTU/ECE/51567

Dated: 13/04/2026

Notice

Please find enclosed herewith the datesheet for End-Sem practical Examination ECE department April 2026.

M. K. Kumar
13/04/26
Dr. Manjeet Kumar

Dy. Superintendent (Practical Examination), ECE
Delhi Technological University

Pankaj Dahiya
13/04/26

Dr. Pankaj Dahiya
Superintendent (Practical Examination), ECE
Delhi Technological University

Neeta
13/4/26
(Prof. Neeta Pandey)
HoD, ECE Deptt.

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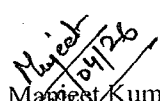
1. The Head of the Applied Chemistry Deptt,
2. The Head of the Applied Physics Deptt.
3. The Head of the Software Engg. Deptt.
4. The Head of the Mechanical Engineering Deptt.
5. The Head Computer Centre with request to upload on DTU website
6. The CoE
7. Circular File
8. Notice board

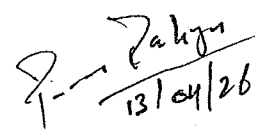
ECE/2636
13/4/26



DELHI TECHNOLOGICAL UNIVERSITY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
DATESHEET FOR
END TERM PRACTICAL EXAMINATION FOR PRS AND PRE COMPONENT
APRIL 2026

1st Year			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
23/04/2026	Sec B_G1 EC102_MW_LAB	Sec B_G2 EC102_MW_LAB	Sec B_G3 EC102_MW_LAB
	Sec C_G1 EC104_ADSP_LAB	Sec C_G2 EC104_ADSP_LAB	Sec C_G3 EC104_ADSP_LAB
24/04/2026	Sec A_G2 EC102_MW_LAB	Sec A_G1 EC102_MW_LAB	Sec A_G3 EC102_MW_LAB
	Sec B_G1 EC104_ADSP_LAB	Sec B_G2 EC104_ADSP_LAB	Sec B_G3 EC104_ADSP_LAB
	Sec VLS_G1 EC106_CF_LAB	Sec VLS_G2 EC106_CF_LAB	Sec VLS_G3 EC106_CF_LAB
25/04/2026	Sec A_G1 EC106_EDC1_LAB	Sec A_G2 EC106_EDC1_LAB	Sec A_G3 EC106_EDC1_LAB
	Sec B_G1 EC106_CF_LAB	Sec B_G2 EC106_CF_LAB	Sec B_G3 EC106_CF_LAB
27/04/2026	Sec C_G1 EC106_CF_LAB	Sec C_G2 EC106_CF_LAB	Sec C_G3 EC106_CF_LAB
	Sec VLS_G1 EC102_MW_LAB	Sec VLS_G2 EC102_MW_LAB	Sec VLS_G3 EC102_MW_LAB
28/04/2026	Sec A_G1 EC104_ADSP_LAB	Sec A_G2 EC104_ADSP_LAB	Sec A_G3 EC104_ADSP_LAB
	Sec C_G1 EC102_MW_LAB	Sec C_G2 EC102_MW_LAB	Sec C_G3 EC102_MW_LAB


 Dr. Manoj Kumar
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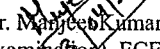
2nd Year			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
23/04/2026	Sec A_G1 EC204_DE_LAB	Sec A_G2 EC204_DE_LAB	Sec A_G3 EC204_DE_LAB
	Sec B_G1 EC206_TELECOM_LAB	Sec B_G2 EC206_TELECOM_LAB	Sec B_G3 EC206_TELECOM_LAB
	Sec C_G1 EC210_EDC2_LAB	Sec C_G2 EC210_EDC2_LAB	Sec C_G3 EC210_EDC2_LAB
	Sec D_G1 EC208_MPI_LAB	Sec D_G2 EC208_MPI_LAB	Sec D_G3 EC208_MPI_LAB
24/04/2026	Sec A_G1 EC210_EDC2_LAB	Sec A_G2 EC210_EDC2_LAB	Sec A_G3 EC210_EDC2_LAB
	Sec B_G1 EC204_DE_LAB	Sec B_G2 EC204_DE_LAB	Sec B_G3 EC204_DE_LAB
	Sec C_G1 EC208_LIC_LAB	Sec C_G2 EC208_LIC_LAB	Sec C_G3 EC208_LIC_LAB
	Sec D_G1 EC206_TELECOM_LAB	Sec D_G2 EC206_TELECOM_LAB	Sec D_G3 EC206_TELECOM_LAB
27/04/2026	Sec A_G1 EC208_LIC_LAB	Sec A_G2 EC208_LIC_LAB	Sec A_G3 EC208_LIC_LAB
	Sec B_G1 EC210_EDC2_LAB	Sec B_G2 EC210_EDC2_LAB	Sec B_G3 EC210_EDC2_LAB
	Sec C_G1 EC206_TELECOM_LAB	Sec C_G2 EC206_TELECOM_LAB	Sec C_G3 EC206_TELECOM_LAB
	Sec D_G1 EC204_DE_LAB	Sec D_G2 EC204_DE_LAB	Sec D_G3 EC204_DE_LAB
28/04/2026	Sec A_G1 EC206_TELECOM_LAB	Sec A_G2 EC206_TELECOM_LAB	Sec A_G3 EC206_TELECOM_LAB
	Sec B_G1 EC208_MPI_LAB	Sec B_G2 EC208_MPI_LAB	Sec B_G3 EC208_MPI_LAB
	Sec C_G1 EC204_DE_LAB	Sec C_G2 EC204_DE_LAB	Sec C_G3 EC204_DE_LAB
	Sec D_G1 EC210_EDC2_LAB	Sec D_G2 EC210_EDC2_LAB	Sec D_G3 EC210_EDC2_LAB

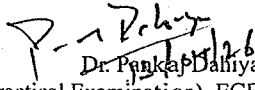
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P. Dahiya
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3rd Year			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
23/04/2026	Sec A_G1 EC302_VLSI_LAB	Sec A_G2 EC302_VLSI_LAB	Sec A_G3 EC302_VLSI_LAB
	Sec B_G1 EC304_MP2_LAB	Sec B_G2 EC304_MP2_LAB	Sec B_G3 EC304_MP2_LAB
24/04/2026	Sec C_G1 EC302_VLSI_LAB	Sec C_G2 EC302_VLSI_LAB	Sec C_G3 EC302_VLSI_LAB
	Sec D_G1 EC304_MP2_LAB	Sec D_G2 EC304_MP2_LAB	Sec D_G3 EC304_MP2_LAB
27/04/2026	Sec A_G1 EC304_MP2_LAB	Sec A_G2 EC304_MP2_LAB	Sec A_G3 EC304_MP2_LAB
	Sec B_G1 EC302_VLSI_LAB	Sec B_G2 EC302_VLSI_LAB	Sec B_G3 EC302_VLSI_LAB
28/04/2026	Sec C_G2 EC304_MP2_LAB	Sec C_G1 EC304_MP2_LAB	Sec C_G3 EC304_MP2_LAB
	Sec D_G1 EC302_VLSI_LAB	Sec D_G2 EC302_VLSI_LAB	Sec D_G3 EC302_VLSI_LAB


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APRIL 2026

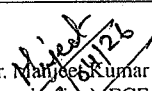
SE			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
23/04/2026	SE202_LIC_LAB_SW1_G1	SE202_LIC_LAB_SW1_G2	SE202_LIC_LAB_SW1_G3
28/04/2026	SE202_LIC_LAB_SW2_G1	SE202_LIC_LAB_SW2_G2	SE202_LIC_LAB_SW2_G3

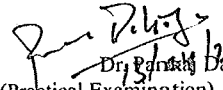
Majeed
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Pankaj
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APRIL 2026

M. TECH VLSI, SPD, MOC	
Date	Session 4 04:00 PM-06:00 PM
23/04/2026	VLS502_VLSI_LAB
	SPD502_ADSP_LAB
	MOC546_MW_LAB
24/04/2026	VLS504_VLSI_LAB
	SPD504_ADSP_LAB
	MOC504_MW_LAB
25/04/2026	VLS532_MP1_LAB
	SPD532_ADSP_LAB
27/04/2026	VLS534_ADSP_LAB
	SPD534_MP1_LAB
	MOC534_MW_LAB
28/04/2026	VLS546_VLSI_LAB
	SPD546_ADSP_LAB
	MOC502_MW_LAB

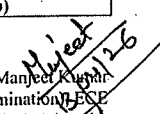

 Dr. Manjiv Kumar
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 Dr. Parvati Dahiya
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ME102, PE102, AE102: BASIC ELECTRONICS AND COMMUNICATION ENGINEERING				
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM	Session 4 04:00 PM-06:00 PM
23/04/2026	ME102_EDC1_LAB_ME2_G1	ME102_EDC1_LAB_ME2_G2	ME102_EDC1_LAB_ME2_G3	
	PE102_BOE_LAB_PE_G1	PE102_BOE_LAB_PE_G2	PE102_BOE_LAB_PE_G3	
24/04/2026	ME102_EDC1_LAB_ME1_G1		ME102_EDC1_LAB_ME1_G3	ME102_EDC1_LAB_ME1_G2
	ME102_BOE_LAB_ME3_G1	ME102_BOE_LAB_ME3_G2	ME102_BOE_LAB_ME3_G3	
27/04/2026	AE102_BOE_LAB_AE_G1		AE102_BOE_LAB_AE_G3	AE102_BOE_LAB_AE_G2
28/04/2026	ME102_BOE_LAB_ME4_G1	ME102_BOE_LAB_ME4_G2	ME102_BOE_LAB_ME4_G3	

BOE LAB (Basic Electronics Lab)	EDC1 LAB (Electronics Devices Circuit-1 Lab)
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